



14th IEEE EAST-WEST DESIGN & TEST SYMPOSIUM (EWDTs 2016)

Yerevan, Armenia, October 14-17, 2016

Call for Papers

The main target of the **IEEE East-West Design & Test Symposium (EWDTs)** is to exchange experiences between scientists and technologies from Eastern and Western Europe, as well as North America and other parts of the world, in the field of design, design automation and test of electronic circuits and systems. The symposium is typically held in countries around the Black Sea, the Baltic Sea and Central Asia region. We cordially invite you to participate and submit your contributions to EWDTs'16 which covers (but is not limited to) the following topics:

- Analog, Mixed-Signal and RF Test
- Analysis and Optimization
- ATPG and High-Level Test
- Automotive Reliability & Test
- Built-In Self Test
- Debug and Diagnosis
- Defect/Fault Tolerance and Reliability
- Design Verification and Validation
- EDA Tools for Design and Test
- Embedded Software Performance
- Failure Analysis & Fault Modeling
- Functional Safety
- High-level Synthesis
- High-Performance Networks and Systems on a Chip
- Internet of Things Design & Test
- Low-power Design
- Memory and Processor Test
- Modeling & Fault Simulation
- Network-on-Chip Design & Test
- Modeling and Synthesis of Embedded Systems
- Object-Oriented System Specification and Design
- On-Line Testing
- Power Issues in Design & Test
- Real Time Embedded Systems
- Reliability of Digital Systems
- Scan-Based Techniques
- Self-Repair and Reconfigurable Architectures
- Signal and Information Processing in Radio and Communication Engineering
- System Level Modeling, Simulation & Test Generation
- System-in-Package and 3D Design & Test
- Using UML for Embedded System Specification
- Optical signals in communication and Information Processing
- CAD and EDA Tools, Methods and Algorithms
- Hardware Security and Design for Security
- Logic, Schematic and System Synthesis
- Place and Route
- Thermal and Electrostatic Analysis of SoCs
- Wireless and RFID Systems Synthesis

The Symposium will take place in Yerevan – the capital and largest city of Armenia, and one of the world's oldest continuously inhabited cities.

Symposium Deadlines:

Submission deadline: September 3, 2016

Notification of acceptance: September 15, 2016

Papers submission info: <http://www.ewdtest.com/conf>

Organizing committee address: Prof. Vladimir Hahanov, Design Automation Department, Kharkov National University of Radioelectronics, Lenin ave, 14, Kharkov, 61166, Ukraine.

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The symposium is organized by Kharkov National University of Radio Electronics in cooperation with Yerevan State University.

Conference content will be submitted for inclusion into IEEE Xplore as well as other Abstracting and Indexing (A&I) databases. IEEE reserves the right to exclude a paper from distribution after the conference, including IEEE Xplore® Digital Library, if the paper is not presented by the author at the conference.



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